

FIG. 1
high-Q on-chip
inductor 10

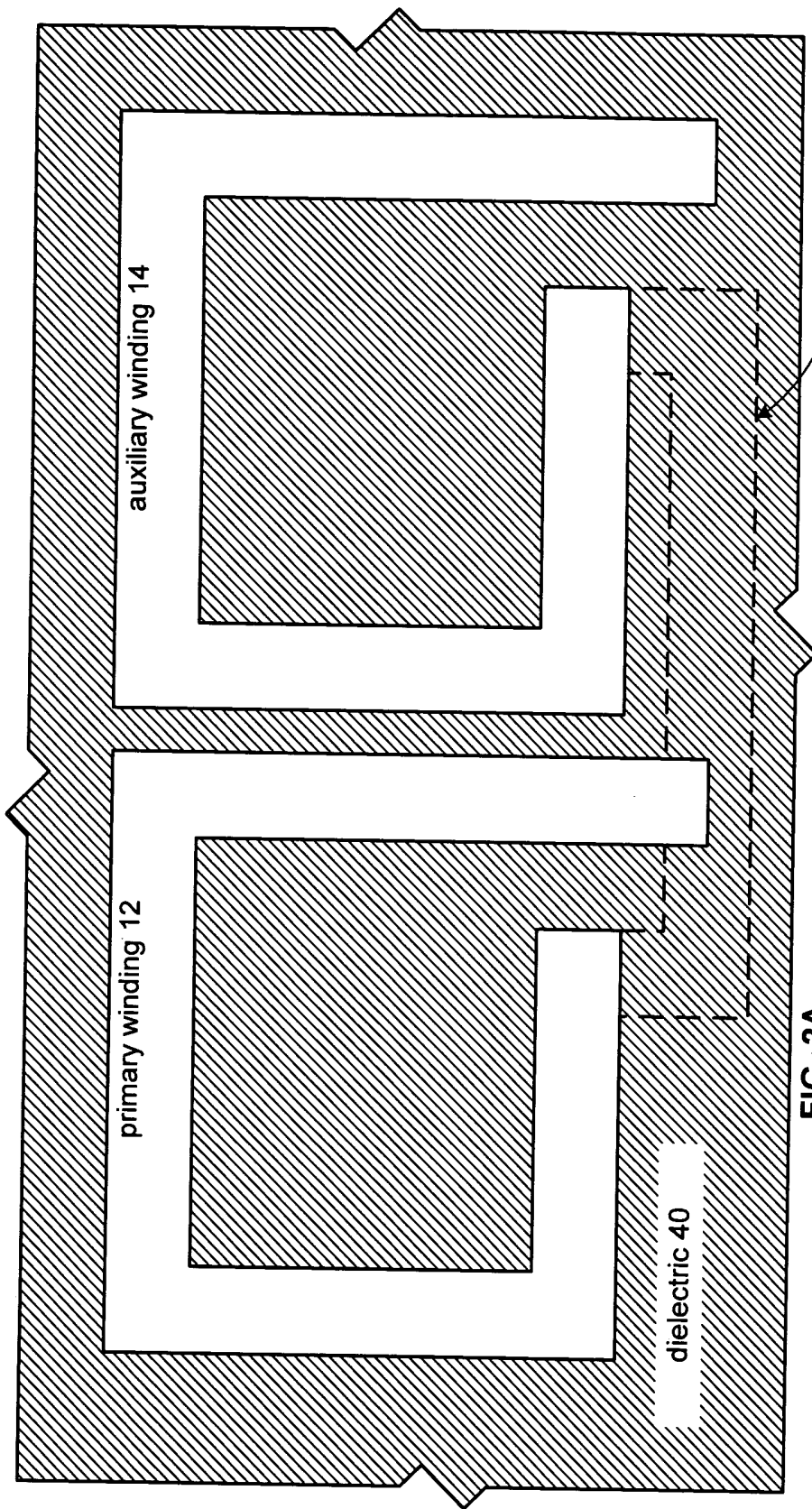


FIG. 2A
TOP VIEW

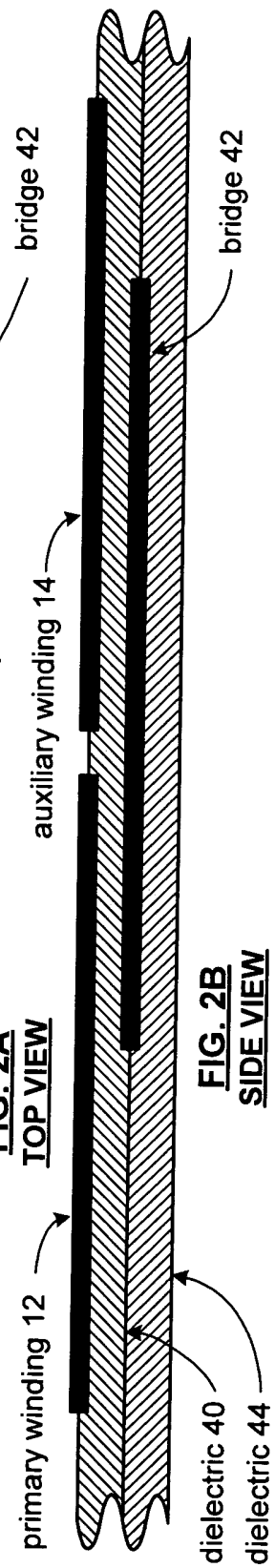


FIG. 2B
SIDE VIEW

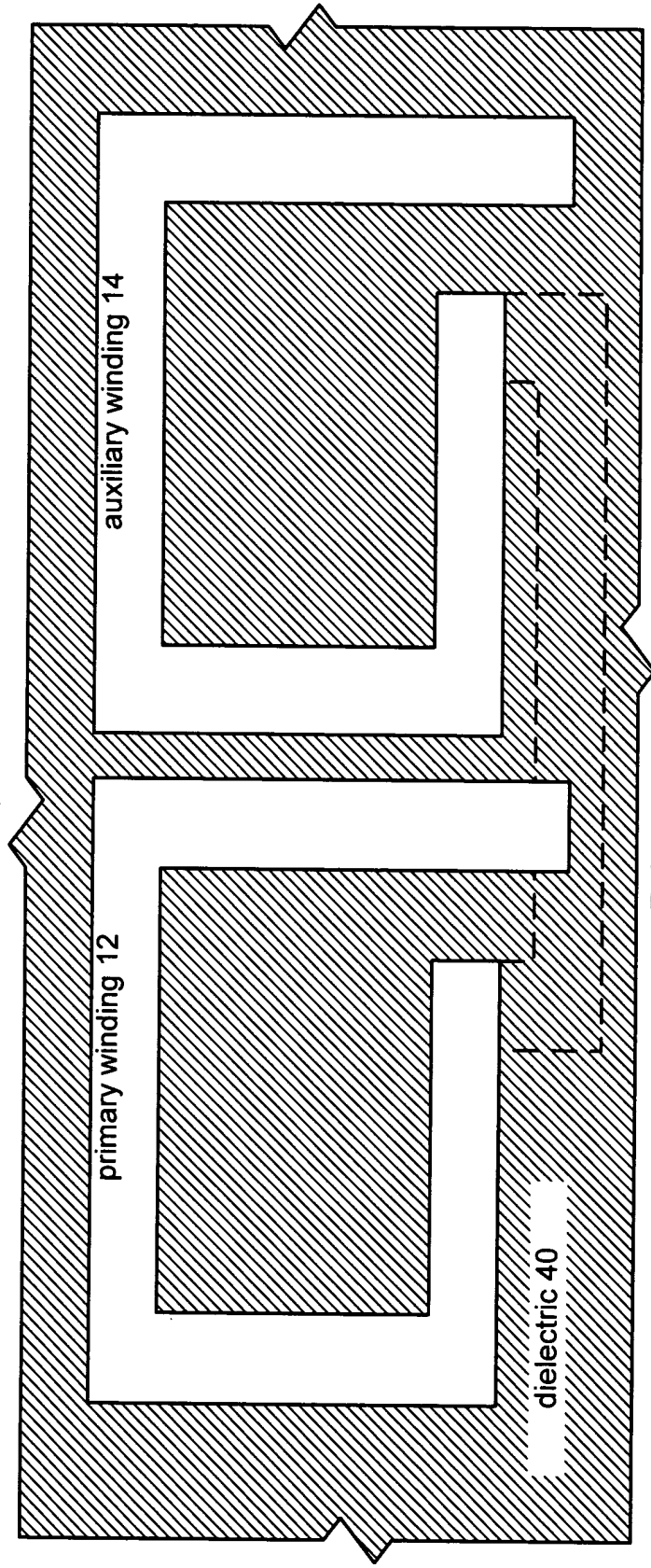


FIG. 3A
TOP VIEW

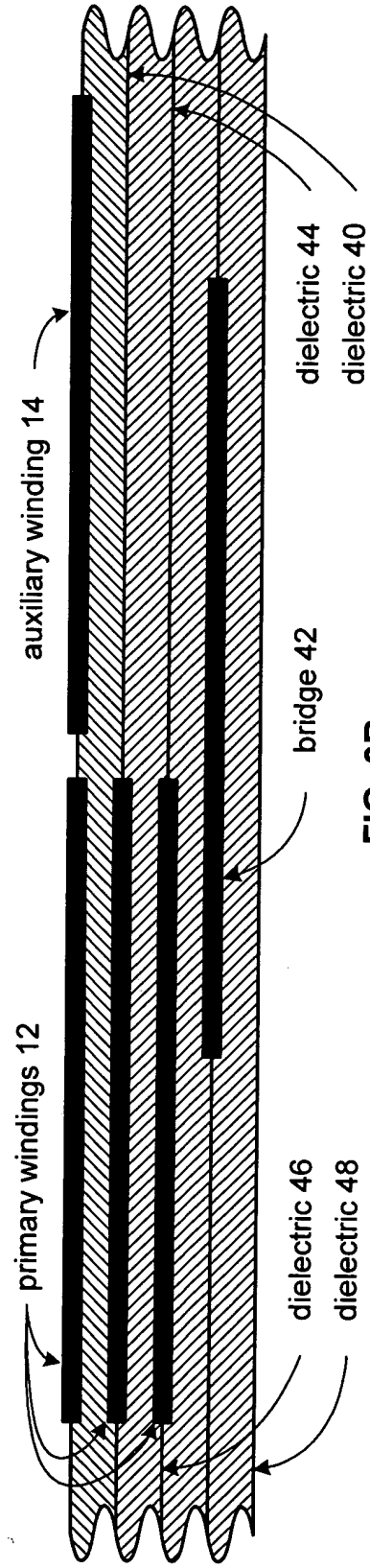


FIG. 3B
SIDE VIEW

FIG. 4A

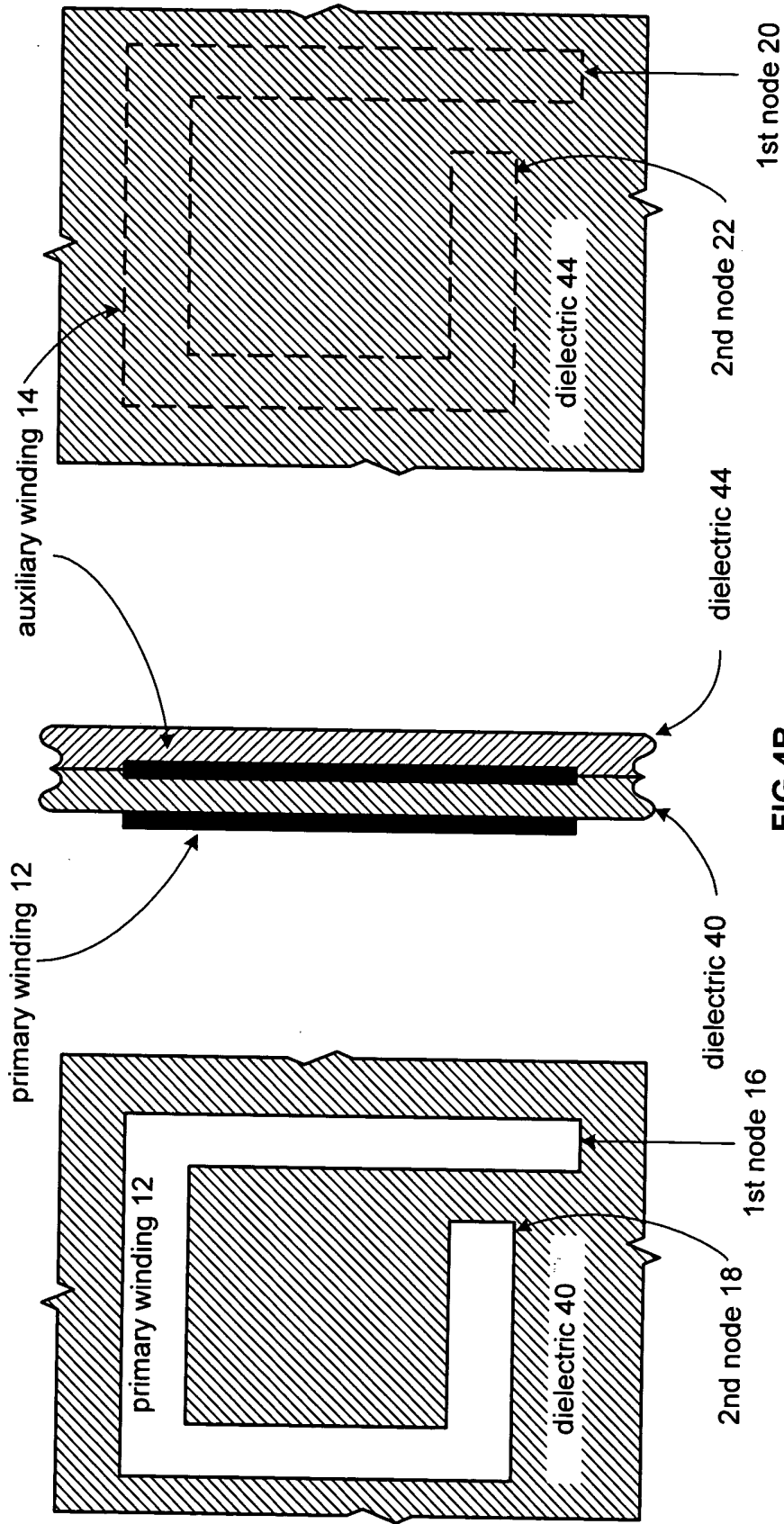


FIG. 4B
SIDE VIEW

FIG. 4A
TOP VIEW

FIG. 4C
BOTTOM
VIEW

44-4444-4444

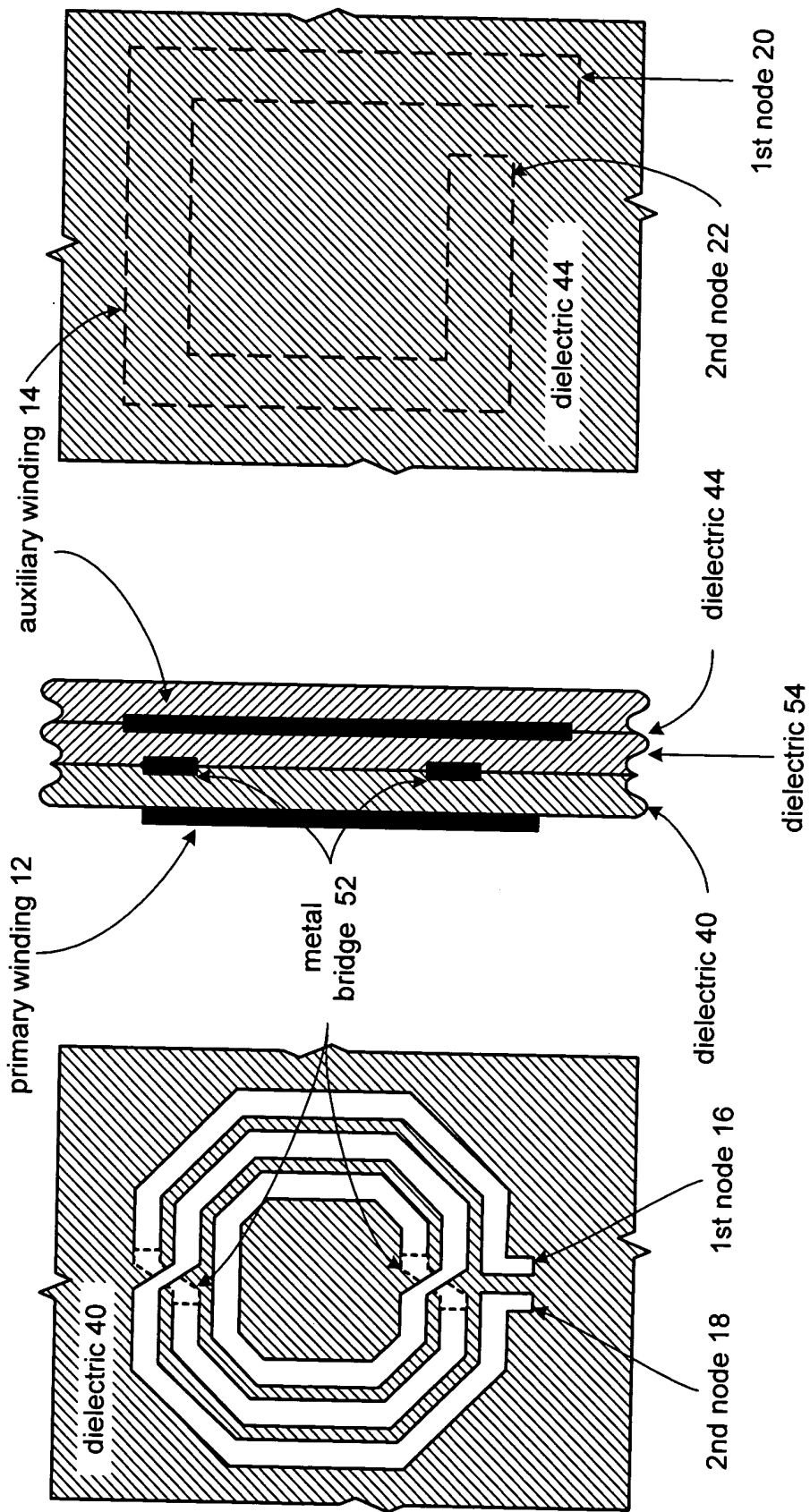


FIG. 5A
TOP VIEW

FIG. 5B
SIDE VIEW

FIG. 5C
BOTTOM VIEW

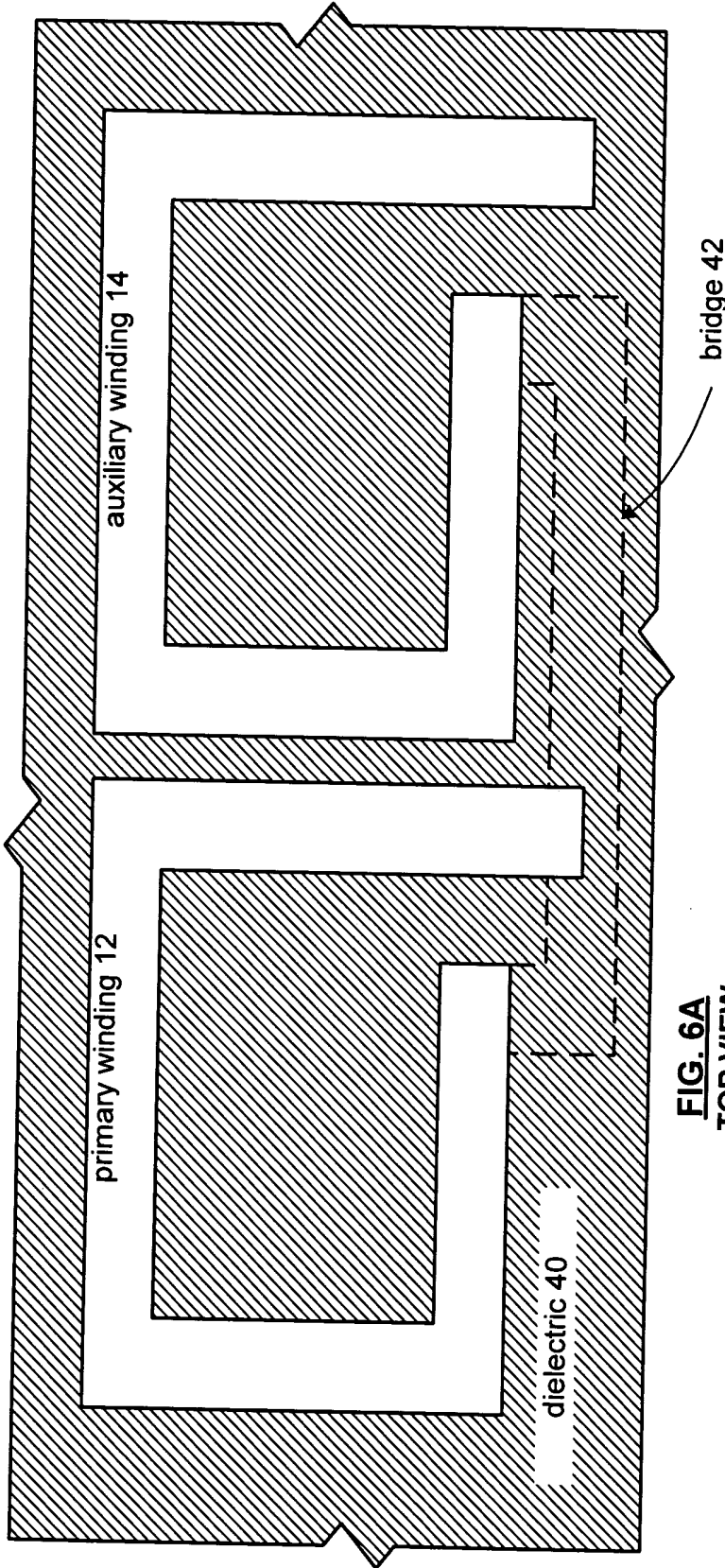


FIG. 6A
TOP VIEW

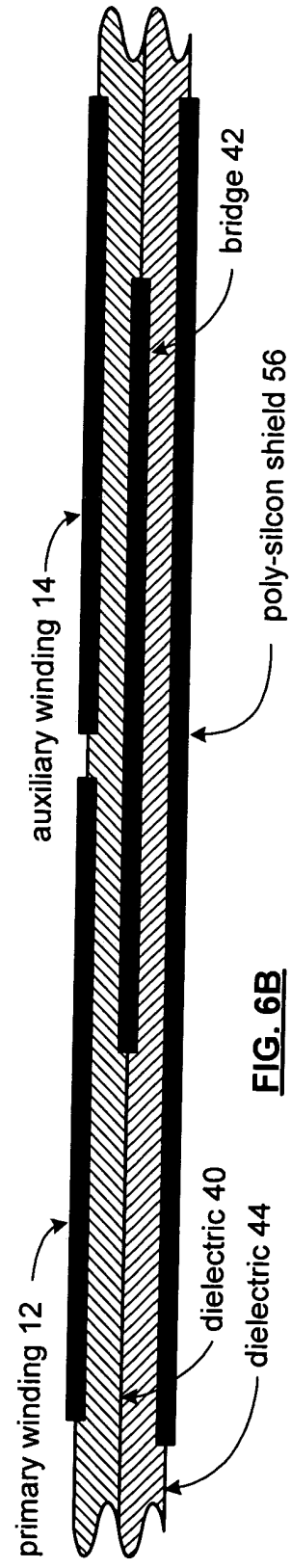


FIG. 6B
SIDE VIEW

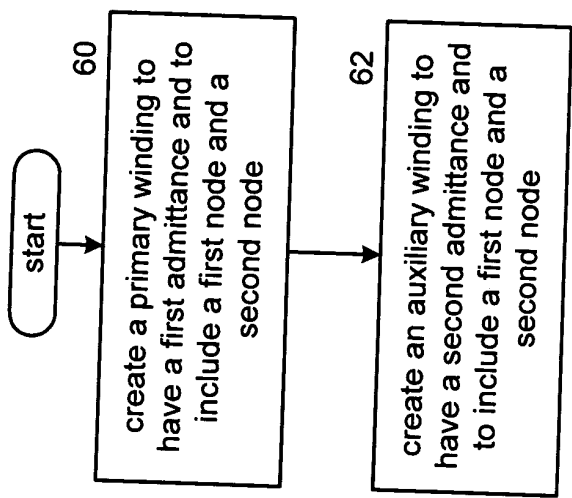


FIG. 7